

PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of : Jong Shik YOON et al. Confirmation No.: 8658  
Application Number : 10/825,343  
Filed : April 15, 2004  
Title : MINIMIZING TRANSISTOR VARIATIONS DUE TO SHALLOW  
TRENCH ISOLATION STRESS  
TC/Art Unit : 2893  
Examiner: : Thanh T. Nguyen  
  
Docket No. : TI-37043 (0025.0245)  
Customer No. : **23494**

**Mail Stop AF**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

**PETITION FOR EXTENSION OF TIME**

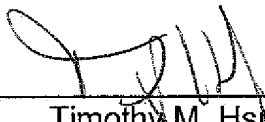
Applicants hereby petition for a three month extension of time to reply to the Office Action of July 22, 2008. Please charge the fee of \$1,110.00 to Texas Instruments' deposit account No. 20-0668.

Please grant any extensions of time required to enter this response and charge any additional required fees to Texas Instruments' deposit account 20-0668.

Respectfully submitted,

Dated: January 22, 2009

By: \_\_\_\_\_

  
Timothy M. Hsieh  
Reg. No. 42,672